

10/523996

Substitute for form 1449A/PTO				07/09/05 Known PTO 09 FEB 2005 Application Number Filing Date February 9, 2005 First Named Inventor Takako KAWASAKI Art Unit Examiner Name Attorney Docket Number 07241.0035-00000	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)					
Sheet	1	of	1		

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
lt		US-6,584,380	06-24-2003	NEMOTO	
		US-			
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		US-			
		US-			

Note: Copies of the U.S. Patent Documents are not Required in IDS filed after October 21, 2004

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
lt		JP 63-173909	11-11-1988			NO
lt		JP 05-347512	12-27-1993	MEIDENSHA CORP		Abstract
lt		JP 08-116214	05-07-1996	FUJITSU LTD		Abstract
lt		JP 11-068461	03-09-1999	TOYO COMMUN EQUIP CO LTD		Abstract
lt		JP 2002-141426	05-17-2002	DAISHINKU CORP		Abstract

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶
lt		KINSEKI, "Temperature Compensation Circuitry of a Crystal Oscillator"; Abstract of Japanese Utility Model Application No. 65046/1987 (Laid-open No. 173909/1988), (November 11, 1988)	
lt		NEMOTO, "A Fully Integrated CMOS Analog TCXO"; 2001 Nen Proceedings of the Electronics, Information and Systems Conference; p. II-219 - II-220; (September 06, 2001)	PARTIAL
lt		NEMOTO, "Approximate Third-Order Function Generator, Temperature Compensation Quartz Oscillation Circuit Made by Using the Same, and Temperature Compensation Method", WIPO Abstract of PCT/JP98/02410, (12-10-1998)	

Examiner Signature	lt	Date Considered	07/21/06
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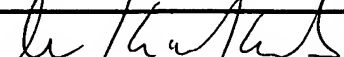
IDS Form PTO/SB/08: Substitute for form 1449				Completed Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/523,996
				Filing Date	February 9, 2005
				First Named Inventor	Takado Kawasaki et al.
				Art Unit	To be Assigned
				Examiner Name	To be Assigned
Sheet	1	of	1	Attorney Docket Number	7241-35

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		US-			
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
ak		GB 2,341,020A	12-10-98	Asahi Kasei Microsystems Co Ltd		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶

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